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Dated: March 16, 2005

Signature: 

(Raymond Garguilo, Jr.)

Docket No.: TESSERA 3.0-320
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Kang et al.

Application No.: 10/655,952

Group Art Unit: 2812

Filed: September 5, 2003

Examiner: S. B. Geyer

For: ASSEMBLIES HAVING STACKED
SEMICONDUCTOR CHIPS AND METHODS
OF MAKING SAME

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This communication is in response to the Office Action mailed February 4, 2005, setting forth a Restriction Requirement in the above-identified application. Enclosed herewith is a Petition requesting a one-month extension of time for resetting the deadline for responding to the Office Action from March 4, 2005 to and including April 4, 2005. Applicants hereby elect Group I, namely claims 1-25 drawn to a semiconductor device, for examination in the present application.

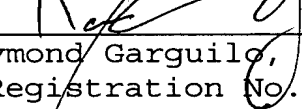
The February 4, 2005 Office Action additionally included an Election of Species Requirement. Applicants hereby elect Species 1 (Figs 1-3b), to which claims 1, 2, 3, 4, 5, 6, 12, 14, 15, 17, 18 and 19 of Group I are readable thereon.

Additionally, Applicants contend that claim 1 should be deemed generic, as it is readable on all of the Species listed in the Official Action. As such, upon the allowance of a generic claim, Applicants will be entitled to consideration of claims to additional species, which are written in dependent form or otherwise include all the limitations of an allowed generic claim.

In the event any fee is due in connection with the present response, the Examiner is authorized to charge Applicant's Deposit Account No. 12-1095 therefor.

Dated: March 16, 2005

Respectfully submitted,

By 
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